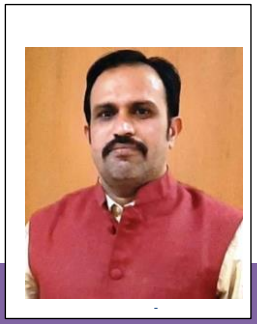


RKDF University, Bhopal

Faculty Profile

Basic Information	
Name	**Dr. Yogesh Mishra**
Date of Birth	**03.03.1985**
Designation	**Assistance Professor**
Department	**Electronics & communication Engineering**
Experience	**11 year**
Email ID	**yogesh.mishra156@gmail.com**
Contact No	**9406730706**

Educational Qualifications

Description	Year	%	Institute/University
(UG)-B.E_ECE	2008	66.66	**RGPV BHOPAL**
(PG)-M.Tech (Digital Communication)	2014	75.40	**RGPV BHOPAL**
M. Phil.	-	-	-
(Ph. D.)- ECE	2024	-	**RKDF UNIVERSITY, BHOPAL**
Post Doctorate	-	-	-
NET Qualified/GATE	-	-	-

Experience Detail

Experience (Teaching/Research)	Designation	Duration		Name of Institute/University
		From	To	
Teaching	Lecturer	20.08.2009	19.08.2010	**Bhabha College of Engineering, Bhopal**
Teaching	Assistant Professor	16.12.2014	03.09.2018	**Sri Satya Sai college of Engineering, RKDF University, Bhopal**
Research	Ph. D Scholar	04.09.2018	12.04.2024	**RKDF University, Bhopal**
Teaching and Research	Assistant Professor	13.04.2024	Till Date	**Sri Satya Sai college of Engineering, RKDF University, Bhopal (M.P.)**

Publications

No. of Papers Published	**15**
No. of Books Published	**-**
Books Chapters Published	**-**
No. of Patents Published/Grant	

Ph. D/M. Phil Project supervised			
Research Program	**Award**	**Under Supervision**	**Nameof University**
Ph. D (Provide detail i.e. name, title etc)	-	-	-
M. Phil	-	-	-
PG Thesis/Dissertation	07	01	**RKDF UNIVERSITY, BHOPAL**

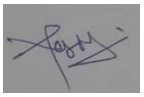
Area of Expertise (100 words)
Spectrum scarcity has emerged as one of the most significant difficulties facing the fifth generation (5G) of wireless communication networks as a direct result of the meteoric rise in the use of intelligent user devices, the Internet of Things (IoT), and autonomous vehicles. Cognitive radio is a promising technology that has the potential to increase spectrum utility.

Award and Achievement	
Name of Award	**Description**
National	**-**
International	**-**

Conference/Seminar/Workshops/FDP	
Description	**No.**
Conference/Seminar p a p e r presentation	**2**
Conference/Seminar attended/ organized	**5**
Workshop attended/organized	**2**
FDP Attended/organized	**2**

Research Project		
Name of Project	**Funding Agencies**	**Amount**
	-	

- **Any other Achievement**



Signature

Paper Publication List

1. Mishra, Yogesh, and Virendra S. Chaudhary. "Deep learning approach for co-operative spectrum sensing under congested cognitive IoT networks." *Journal of Integrated Science and Technology* 12.4 (2024): 778-778. DOI: https://doi.org/10.62110/sciencein.jist.2024.v12.778

2. Mishra, Yogesh, and Virendra S. Chaudhary. "Spectrum sensing in cognitive radio for internet of things using deep learning models." *SAMRIDDHI: A Journal of Physical Sciences, Engineering and Technology* 15.01 (2023): 27-33. **DOI** https://doi.org/10.18090/samriddhi.v15i01.04

3. Mishra, Yogesh, Ashish Singhadia, and Rashmi Pandey. "Energy level based stable election protocol in wireless sensor network." *International Journal of Engineering Trends and Technology (IJETT)–Volume* 17 (2014): 32-38. DOI: 10.14445/22315381/IJETT-V17P206

4. Mishra, Yogesh, Ashish Singhadia, and Rashmi Pandey. " An Exploratory Survey of Energy Efficient Protocols and Techniques in Wireless Sensor Network" International Journal of Recent Development in Engineering and Technology" (IJRDET) (ISSN 2347-6435(Online) Volume 3, Issue 1, July 2014) https://www.ijrdet.com/files/Volume3Issue1/IJRDET_0714_11.pdf

5. Amit Sanatan, Yogesh Mishra, Sachin Bandewar"A Hardware-Efficient VLSI Implementation of Analog to Digital Converter" SHODH SANGAM - A RKDF University Journal of Science and Engineering ISSN No. 2581-5806 Vol.-03, No.-05, Sept-2020.

6. Implementation and Analysis of 2D DWT Architecture for JPEG 2000. MYM Naurini Fatima. shodh sangam 6 (05), 27-35, 2023. 2023.

7. A Survey on VLSI Designs for Image Compression Standards. MYM Naurin Fatima. shodh sangam 6 (04), 20-25, 2023. 2023 .

8. A Simulation Analysis of Algorithm based on Global MSE Optimization for Target Trackin, IRJTAS 2017,vol,1.

9. Fault Tolerant Parallel Filters on ECC with FFTs - A Survey, INTERNATIONAL JOURNAL OF INNOVATIVE TRENDS IN ENGINEERING (IJITE) ISSN: 2395-2946 ISSUE: 60, VOLUME 39, NUMBER 02, MARCH 2018.

10. Development of 64-Bit Efficient Delay Profile Multiplier Based on Dadda Algorithm, INTERNATIONAL JOURNAL OF SCIENTIFIC PROGRESS AND RESEARCH (IJSPR) ISSN: 2349-4689 Volume-15, Number - 02, 2015. https://www.ijspr.com › citations › IJSPR_1502_201

11. Adaptive feature fusion for missing spectrum-data Recovery in Cognitive IoT Network,

International Journal of Engineering Research ISSN:2319-6890 (online),2347-5013(print) Volume No.12, Issue No.4, pp : 104-111, 1 Dec. 2023, https://nityapublications.com.

12. Implementation of VLSI Architecture for Object Tracking Using Serial Division Algorithm, YM Rajiv Kumar1,Sachin Bandewar2 shodh sangam 3, 111-118. https://scholar.google.co.in

13. A Hardware-Efficient VLSI Implementation of Analog to Digital Converter, SB Amit santan ,yogesh Mishra shodh sangam 3, 96-100, . https://scholar.google.co.in

14. Area Optimizes FFTs for Fault Tolerant Architectures with Vedic Multiplication Sutra and Error Corrections, YM Priyanka Kumari IJSPR 45 (132), 200-206, ISSN: 2349-4689, https://www.ijspr.com/article_details.php?paperid=4504_92268

15. An analysis on designing issues of unsigned multiplier using CLAA and CSLA, yogesh Mishra IJITE, 2015, https://scholar.google.co.in